

TL866 LATCH PIN MAP

NPN driver(VPP/GND) PNP driver(VCC) Bit order	80 7F D7	40 BF D6	20 DF D5	10 EF D4	8 F7 D3	4 FB D2	2 FD D1	1 FE D0
LE0 [L0-VPP]	37	33	9	10	4	3	34	31
LE1 [L1-VPP]	32	38	39	40	2	1	36	30
LE2 [L2-VCC]	1	38	3	2	37	5	40	39
LE3 [L3-VCC]	36	30	33	35	34	6	32	4
LE4 [L4-VCC]	12	7	21	13	10	9	8	11
LE5 [L5-GND]	10	7	12	11	6	5	8	9
LE6 [L6-GND]	32	3	35	34	2	1	4	31
LE7 [L7-GND]	39	16	36	37	14	30	38	40